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(Use as many sheets as necessary)

Application Number	10/017,777
Filing Date	December 13, 2001
First Named Inventor	Kuzemchak
Art Unit	2122
Examiner Name	Vo
Attorney Docket Number	TI-32443

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Bryant et al., "Verification of Pipelined Microprocessors by Comparing Memory Execution Sequences in Symbolic Simulation" ASIAN '97 (1997), pp. 1-14.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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